

**PATENT APPLICATION**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of

Attorney Docket Q63456

Geoffrey DIVE

Appln. No.: Unassigned

Group Art Unit: Unassigned

Confirmation No.: Unassigned

Examiner: Unassigned

Filed: March 19, 2001

For: METHOD, MODULES AND PROGRAM MODULES FOR SYNCHRONIZATION

**PRELIMINARY AMENDMENT**

Commissioner for Patents  
Washington, D.C. 20231

Sir:

Prior to examination, please amend the above-identified application as follows:

**IN THE SPECIFICATION:**

Page 1, after the title, delete the heading "Description".

**IN THE CLAIMS:**

**Please cancel claim 11 second occurrence beginning on page 22, line 6, with the words "A memory means".**

**Please add the following new claims:**

12. A computer-readable diskette storing a master program module according to Claim 9.

13. A computer readable diskette storing a slave program module according to Claim 10.

PRELIMINARY AMENDMENT  
Attorney Docket Q63465

**IN THE ABSTRACT:**

**Please delete the present Abstract of the Disclosure and replace it with the following  
new Abstract of the Disclosure.**

## ABSTRACT


The present invention relates to a method for the synchronization of a first and a least one second module, each having a clock generator. The invention furthermore relates to such modules, a master program module, a slave program module and a device for this purpose. It is proposed that the first module, transmits a first clock signal generated by its clock generator to the second module, which synchronizes its clock generator with the first clock signal. The second module transmits a second clock signal generated by its clock generator that is synchronized with the first clock signal to the first module, which determines a time difference value between the first clock signal and the second clock signal, which time difference value is essentially due the transmission time of the first and the second clock signal between the first and the second module. The first module transmits an item of information about the (first) time difference value to the second module, which adjusts its clock generator on the basis of said information.

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**REMARKS**

Entry and consideration of this Amendment is respectfully requested.

Respectfully submitted,

  
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**APPENDIX**

**VERSION WITH MARKINGS TO SHOW CHANGES MADE**

**IN THE SPECIFICATION:**

**The specification is changed as follows:**

Page 1, after the title, delete the heading "Description".

**IN THE CLAIMS:**

**Claim 11 second occurrence beginning on page 22, line 6, with the words "A memory means" was cancelled.**

**New claims 12 and 13 were added. See above.**

**IN THE ABSTRACT OF DISCLOSURE:**

**The Abstract was deleted and a new Abstract was added. See above section.**